

## **AMENDMENTS TO THE SPECIFICATION**

Please replace the paragraph on page 9, lines 6-18, which begin with the phrase “The synthesizer section 227” with the following rewritten paragraph:

- - A ~~[[The]]~~ synthesizer section 227 of the RF front end 107 is typically entirely contained in the RFIC except for ~~[[the]]~~ loop filter components 228, and the support components of ~~[[the]]~~ crystal oscillator active circuitry that may be referred to as a crystal oscillator or a reference oscillator 230. The reference ~~crystal oscillator~~ 230 ~~section~~ can be implemented using a crystal resonator 226, or using a signal from an external Temperature Compensated crystal Oscillator (TCXO). The crystal resonator 226 frequency is typically nominally 24.5535 MHz, and can vary by 40 parts per million (ppm) around this frequency. The reference oscillator 230 frequency is doubled by a frequency doubler 232 and then divided by 9 by a divider 234. Of course, the ~~crystal~~ reference oscillator 230 can generate a frequency of twice that of 24.5535 MHz, which eliminates the need for the frequency doubler 232. The doubled frequency (or direct frequency if it is already generated) is also sent to a PECL output buffer 260 to provide a signal 236, typically named GPSCLK, which is a GPS clock signal used by the digital ASIC 110. Typically, the GPSCLK signal 236 has a frequency of approximately  $48f_0$ , where  $f_0 = 1.023$  MHz. The divided-by-9 signal is applied to a phase/frequency detector 238 for the reference input of the phase-lock-loop. - -